Reviewed by: Eric Wagner

Module Name: rtc\_timer

Jira Ticket: SWATCH2-19

**Stopwatch Requirements Checklist Rev. 2**

1. Is requirements document written clearly and grammatically correct?

Yes [X] No [ ]

Comments:

1. Does requirements document define all tasks to be performed?

Yes [X] No [ ]

Comments:

1. Does requirements document include all inputs and outputs to the module along with their corresponding accuracy, range of values, frequency and format?

Yes [X] No [ ]

Comments:

1. Is each requirement unique?

Yes [ ] No [X]

Comments: INIT\_01 is the same as RST\_01

1. Does requirements document comply with system requirements and traceability?

Yes [ ] No [X]

Comments: Format/Naming changes

CNT\_01: ‘high’ to ‘logic high’ also ‘input’ after i\_sclk is redundant

RST\_01: ‘system reset input’ to ‘i\_reset\_n’

1. Is terminology consistent with standards?

Yes [X] No [ ]

Comments:

1. Is it possible to implement all requirements?

Yes [X] No [ ]

Comments:

1. Is each requirement testable or verifiable?

Yes [X] No [ ]

Comments: